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ENGINEERING II ELECTRICAC CIRCUIT ANALYSIS ASSIGNMENT 12

P13-36. Analyze the memory element in Fig. P13.36. Draw up a truth table assuming the input is a series of pulses and Q is initially 1. What function is performed by the AND gates?



Figure P13.36 Memory element.

P14-12. Use Boolean theorems to prove the following identities!

(a)  $A + \overline{A}B = A + B$ 

(b) 
$$ABC + AB\overline{C} = AB$$

(c) 
$$(A + \overline{B})B = AB$$

- (d)  $(A + B)(\overline{A} + C) = AC + \overline{A}B$
- P14-13. When writing equations for "programmed array logic" circuits, complicated expressions must be broken down into simple "sums-ofproducts" (like Eq. 14-6).
  - (a) Write the following expression as a sum-ofproducts. Show the Boolean theorems used during each step of simplification.

 $[(A \cdot B \cdot \overline{C}) \cdot (\overline{A \cdot \overline{B} \cdot C}) + \overline{A}\overline{B}] \cdot \overline{D}$ 

(b) Invert the result from part (a), and factor it into a sum-of-products, showing theorems used. **P14-14.** The function f = A + B is to be realized using only NAND gates. Use DeMorgan's theorems to express f in terms of  $\overline{C \cdot D}$  where C and D can be expressed in terms of A and B. Draw the necessary logic circuit and check by constructing the truth table.

P14-15. Analyze the logic circuit of Fig. P14.15 and determine f in terms of A and B. Simplify using Boolean algebra and check your result with a truth table.



Figure P14.15

P14-20. Given the logic function

## $f = \overline{AB} + \overline{\overline{A}\overline{B}} + \overline{\overline{A}B}$

- (a) Assuming the complements are available, simplify the function using DeMorgan's theorem and synthesize it using the basic gates.
- (b) Assuming the complements are not available, simplify the function and synthesize it from five NAND gates.

Answer: (a) f = AB + BA.

P14-24. Map the following functions and find the minimal sum-of-products form:

(a)  $ABC\overline{D} + A\overline{B}C + \overline{B}\overline{C}$ (b)  $AB + \overline{A}\overline{B}CD + A\overline{B}C$  LAGI

