2005

P3-25. Explaining your reasoning and stating any simplifying assumptions, predict current I in Fig. P3 25.



- **P3-41.** In Fig P3.41, when voltage $v_1 = V_m(0.5 + 1)$ $\sin \omega t$) V, the high-resistance dc voltmeter VM reads 70 V.
 - (a) What functions are performed by each section of the circuits?
 - (b) How is v_2 related to v_1 ? Show v_1 , v_2 , and v_3 on the same graph.
 - (c) Determine V_m and define the function of this instrument.



Figure P3 41

P13-36. Analyze the memory element in Fig P13 36 Draw up a truth table assuming the input is a series of pulses and **Q** is initially 1 What function is performed by the AND gates?



Figure P13 36 Memory element

- P14-12. Use Boolean theorems to prove the following identities?
 - (a) $\mathbf{A} + \overline{\mathbf{A}}\mathbf{B} = \mathbf{A} + \mathbf{B}$ (b) $ABC + AB\overline{C} = AB$ (c) $(\mathbf{A} + \mathbf{\overline{B}})\mathbf{B} = \mathbf{A}\mathbf{B}$ (d) $(\overline{A} + \overline{B})(\overline{A} + \overline{C}) = AC + \overline{A}B$ (e) (A + C)(A + D)(B + C)(B + D) =AB + CD

- P14-13. When writing equations for "programmed array logic" circuits, complicated expressions must be broken down into simple "sums-ofproducts" (like Eq. 14-6)
 - (a) Write the following expression as a sum-ofproducts. Show the Boolean theorems used during each step of simplification.

 $[(A \cdot B \cdot \overline{C}) \cdot (\overline{A \cdot B \cdot C}) + \overline{A}\overline{B}] \cdot \overline{D}$

- (b) Invert the result from part (a), and factor it into a sum-of-products, showing theorems used.
- **P14-14.** The function $\mathbf{f} = \mathbf{A} + \mathbf{B}$ is to be realized using only NAND gates Use DeMorgan's theorems to express f in terms of $\overline{\mathbf{C} \cdot \mathbf{D}}$ where c and **D** can be expressed in terms of **A** and **B** Draw the necessary logic circuit and check by constructing the truth table
- P14-15. Analyze the logic circuit of Fig P14 15 and determine f in terms of A and B. Simplify using Boolean algebra and check your result with a truth table.



Figure P14 15

P14-20. Given the logic function 1

$$= \overline{AB + \overline{A}\overline{B}} + \overline{A}B$$

- (a) Assuming the complements are available, simplify the function using DeMorgan's theorem and synthesize it using the basic gates.
- (b) Assuming the complements are not available, simplify the function and synthesize it from five NAND gates.

Answer: (a) $\mathbf{f} = \mathbf{A}\mathbf{B} + \mathbf{B}\mathbf{A}$.

- P14-24. Map the following functions and find the minimal sum-of-products form:
 - (a) $ABC\overline{D} + A\overline{B}C + \overline{B}\overline{C}$ (b) $AB + \overline{A}\overline{B}CD + A\overline{B}C$ (c) $\overline{A}(C + D) + A\overline{B}C + A\overline{B}\overline{C}\overline{D}$
 - (d) $\overline{ABCD} + \overline{ABCD} + \overline{BCD} + \overline{BCD}$